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Attorney's Docket No.: 42390P4912X

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:
Mohammad Abdallah et al.

Application No. 09/783,875

Filed: February 14, 2001

For: METHOD AND APPARATUS FOR
FLOATING POINT OPERATIONS AND
FORMAT CONVERSION OPERATIONS

Examiner: Whitmore, Stacy

Art Unit: 2825

CERTIFICATE OF TRANSMISSION

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Sir:

Enclosed are Information Disclosure Citation Forms PTO/SB/08A and PTO/SB/08B. The non-patent documents cited on these forms were previously submitted with Information Disclosure Statements in earlier applications, Ser. No. 09/070,891, now US Patent No. 6,266,769 and/or Ser. No. 09/071,466, now US Patent No. 6,282,554, which are being relied upon for an earlier effective filing date under 35 USC §120.

It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Forms be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

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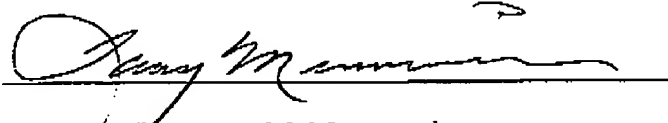
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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: January 23, 2006



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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 5

Complete if Known

Application Number	09/783,875
Filing Date	February 14, 2001
First Named Inventor	Mohammad Abdallah, et. al.
Art Unit	2825
Examiner Name	Whitmore, Stacy
Attorney Docket Number	42390P4912X

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (If known)			
		US- 4949291	Aug., 1990	Saini	
		US- 5157388	Oct., 1992	Kohn	
		US- 5257215	Oct., 1993	Poon.	
		US- 5303174	Apr., 1994	Okamoto.	
		US- 5528741	Jun., 1996	Lucas	
		US- 5627773	May., 1997	Wolrich et al.	
		US- 5673407	Sep., 1997	Poland et al.	
		US- 5696709	Dec., 1997	Smith, Sr.	
		US- 5721855	Feb., 1998	Hinton et al.	
		US- 5729724	Mar., 1998	Sharangpani et al.	
		US- 5740093	Apr., 1998	Sharangpani	
		US- 5761103	Dec., 2000	Oakland et al.	
		US- 5764548	Jun., 1998	Keith et al.	
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		US- 5802219	Sep., 1998	Farkas et al.	
		US- 5805486	Sep., 1998	Sharangpani.	
		US- 5848284	Dec., 1998	Sharangpani.	
		US- 5852726	Dec., 1998	Lin	
		US- 5874969	Feb., 1999	Storm et al.	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (If known)				
		WO99/23548	May, 1999	Oberman et. al.		

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Sheet 2 of 5	Attorney Docket Number	42390P4912X	

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		Number-Kind Code ² (if known)			
		US- 5968116	Oct., 1999	Day, II et al.	
		US- 5978901	Jun., 1998	Luedtke et al.	
		US- 5995122	Nov., 1999	Hsieh et al.	
		US- 6058465	May, 2000	Nguyen	
		US- 5673093	Sep., 1997	Poland, et. al.	
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		Examiner Name	Whitmore, Stacy
Attorney Docket Number	42390P4912X		
Sheet	3	of	5

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
		Sun Microsystems; "Visual Instruction Set (VIS.TM.) User's Guide"; Ver. 1.1, Mar. 1997; pp. 11-12.	
		Advanced Micro Devices (AMD); "AMD-3D.TM. Technology Manual"; Feb., 1998; pp. 19, 20.	
		MIPS Technologies, Inc.; "MIPS Extension for Digital Media with 3D"; Dec. 3, 1996, pp. 1-26.	
		Bistry, et al.; "The Complete Guide to MMX.TM. Technology"; 1997; Chapter 1--pp. 2-22.	
		Bistry, et al.; "The Complete Guide to MMX.TM. Technology"; 1997; Chapter 2--pp. 24-34.	
		Bistry, et al.; "The Complete Guide to MMX.TM. Technology"; 1997; Chapter 4--pp. 54-65.	
		Bistry, et al.; "The Complete Guide to MMX.TM. Technology"; 1997; Chapter 5--pp. 68-110.	
		Bistry, et al.; "The Complete Guide to MMX.TM. Technology"; 1997; Appendix B--pp. 257-268.	
		Tom Shanley; "Pentium.RTM. Pro and Pentium.RTM.II System Architecture"; 1998; Chapter 18--pp. 379-406.	
		Tom Shanley; "Pentium.RTM. Pro and Pentium .RTM.II System Architecture"; 1998; Chapter 26--pp. 507-520.	

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		Rohan Coelho, et al.; "Direct X.RTM., RDX, RSX, and MMX.TM. Technology a Jumpstart Guide to High Performance APIs"; 1998; Chapter 18--pp. 277-286.	
		Rohan Coelho, et al.; "Direct X.RTM., RDX, RSX, and MMX.TM. Technology a Jumpstart Guide to High Performance APIs"; 1998, Chapter 19--pp. 289-308.	
		Rohan Coelho, et al.; "Direct X.RTM., RDX, RSX, and MMX.TM. Technology a Jumpstart Guide to High Performance APIs"; 1998; Chapter 20--pp. 311-330.	
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		Alpha Architecture Handbook Version 3, Oct. 1996, pp. 4-79 to 4-116.	
		"Mechanism to clamp and pack lit floating-point colors", IBM Technical Disclosure Bulletin, Mar. 1997, v40, n3, pp 163-164.	
		"Hyperspeed product benchmarks--X860/XP860", Apr. 3, 1996, at www5.electriciti.com/hyperspd/i860bnch.html.	
		AMD-K6 Processor Data Sheet, Mar. 1998, Advanced Micro Devices, Inc. pp. 21-38.	
		"MIPS V Instruction Set," Oct. 1996, Rev. 1.0; pp. B-1, B-2, B-18.	
		"MIPS Digital Media Extension"; Oct. 1996, Rev. 1.0 pp C-1, C-2.	

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NON PATENT LITERATURE DOCUMENTS

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		Darley, M. et al, "The TMS390C602A Floating-Point Coprocessor for Sparc Systems", IEEE MICRO, Jun. 1990, pp. 36-47.	
		Gilliam, K. et al, "Design and Architecture for a Multi-Mode Pipelined, Floating-Point Adder", Proceedings of the IEEE, 1991, pp. 73-76.	

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